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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/823,937	04/14/2004	Sam Denovich	18150 (AT 20958-2136)	5283

7590 03/10/2005
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EXAMINER

GILMAN, ALEXANDER

ART UNIT PAPER NUMBER

2833

DATE MAILED: 03/10/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/823,937	DENOVIH ET AL.	
	Examiner	Art Unit	
	Alexander D. Gilman	2833	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 14 April 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-7,9,10,12 and 14-20 is/are rejected.
- 7) ☒ Claim(s) 8,11 and 13 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

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DETAILED ACTION

Claim Objections

Claim 13 is objected to because of the following informalities:

Claim 13b recites " said body includes a front, a base and side walls having a wedge shape, said front wall defining said first connectivity interface".

It is unclear if each of a front, a base and side walls having a wedge shape, or these walls form a body having a wedge cross section. It was interpreted that these walls form a body having a wedge cross section

Appropriate correction is required.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1, 2, 6, 7, 12, 14, 17 are rejected under 35 U.S.C. 102(b) as being anticipated by Follingstad et al.

With regard to claims 1, 12, Follingstad et al (US 6,623,278) disclose a patch panel system, comprising:

a frame (22);

a patch panel (26) attachable to said frame, said patch panel including a first connectivity

interface having (Fig. 3) multiple sections joined to form an N-sided portion of a polygon where N is greater than two; and

connector ports (56, 58) provided at said first connectivity interface.

With regard to claims 2, 14 Follingstad et al disclose that said multiple sections of said first

connectivity interface have planar front surfaces that are oriented at obtuse angles to one another

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along a substantially arcuate path (formed by the edged surfaces having holes for screws).

With regard to claim 6 Follingstad et al disclose a multi-port connector (28) provided in a second connectivity interface, said multi-port connector conveying multiple independent data streams associated with multiple independent information sources or destinations.

With regard to claims 7, 17 Follingstad et al disclose a circuit board (44 in said patch panel, said circuit board including at least one of a communications path and a power distribution path individually joined to said connector ports.

With regard to claim 20, Follingstad et al disclose that said connector ports provided at said first connectivity interface are arranged into connector modules, each of said connector ports in a first connector module being communicatively coupled to a single common multi-data stream connector port provided at a second connectivity interface of said patch panel(Fig. 9a).

Claims 1, 12, 19 are rejected under 35 U.S.C. 102(b) as being anticipated by Wettengel et al. With regard to claims 1, 12 Wettengel et al (US 5,011,257) disclose a patch panel system, comprising: a frame (22); a patch panel (2) attachable to said frame, said patch panel including a first connectivity interface having multiple sections joined to form an N-sided portion of a polygon where N is greater than two; and connector ports provided at said first connectivity interface.

Claims 1, 3-5, 9, 10, 12, 15, 16, 19, are rejected under 35 U.S.C. 102(b) as being anticipated by Morgan et al.

With regard to claims 1, 12, Morgan et al (US 5,129,842) disclose a patch panel system, comprising: a frame (12, 14, 18); a patch panel (22) attachable to said frame, said patch panel including a first connectivity interface having multiple sections joined to form an N-sided portion of a polygon where N is greater than two (end tabs 72, 74 forming non plane surface) ; and

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connector ports (65, 66) provided at said first connectivity interface.

With regard to claims 3, 15 Morganl et al disclose (Fig. 2) that at least one of said connector ports includes

a rear face configured to be directly connected to a cable.

With regard to claim 4, Morganl et al disclose that at least one of said connector ports is configured to convey a single data stream associated with a single information source or destination.

With regard to claim 5, 16 Morganl et al disclose that said patch panel includes a second connectivity interface including at least one multi-port connector port the port having connectors (67, 68) communicatively interconnected with a plurality of said connector ports at said first connectivity interface.

With regard to claim 9, Morganl et al disclose (Fig. 6) that said patch panel includes a latch tab (16, 18) thereon extending outward from said srst cornectivity interface, said latch tab being one of securely and hingeably attached to said fmmme.

With regard to claims 10, 18, Morganl et al disclose (Fig. 4) that a plurality of said patch panels arranged adjacent one another in a stacked manner.

With regard to claim 19 Morgan et al disclose (Fig. 6) that said multiple sections of said first connectivityinterface are planar and each hold a plurality of connector ports groups into an array.

Allowable Subject Matter

Claims 8, 11 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claim 13 would be allowable if rewritten to overcome the objection, set forth in this Office action and to include all of the limitations of the base claim and any intervening claims.

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No prior art has been found to anticipate or render obvious the presently claimed subject matter.

Specifically, none of the prior art of record discloses the combination of the limitations presented including the first and second connectivity interfaces being extended along generally concentric arcuate paths (claim 8).

patch panel include a wedge shaped body with opposed front and back walls, at least one of said front and back walls being generally arcuately shaped (11, 13).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Alexander D. Gilman whose telephone number is 571 272-2004. The examiner can normally be reached on Monday-Friday, 10:30 a.m. - 8:00 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Paula A. Bradley can be reached on 571 272-2800 ext. 33. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

03/03/2005



ALEXANDER GILMAN
PRIMARY EXAMINER